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		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L111	L109 and field\$1	17
<input type="checkbox"/>	L110	L109 and executable	0
<input type="checkbox"/>	L109	L108 and bootstrap	17
<input type="checkbox"/>	L108	L107 and descriptor\$1	33
<input type="checkbox"/>	L107	L106 and (memory near5 header)	51
<input type="checkbox"/>	L106	L105 and (data near5 structure)	775
<input type="checkbox"/>	L105	L104 and (data near5 segment\$1)	775
<input type="checkbox"/>	L104	L103 and (target near5 data)	4167
<input type="checkbox"/>	L103	(data near5 structure) and (storage near5 device\$1) and @py<=2004	43079
<input type="checkbox"/>	L102	L101 and directory	24
<input type="checkbox"/>	L101	L100 and (data near5 structure)	31
<input type="checkbox"/>	L100	L99 and (address near5 location\$1)	31
<input type="checkbox"/>	L99	L98 and (data near5 field\$1)	32
<input type="checkbox"/>	L98	L96 and executable	33
<input type="checkbox"/>	L97	L96 and (target near5 cpu)	0
<input type="checkbox"/>	L96	L95 and flag\$1	36
<input type="checkbox"/>	L95	L94 and (target ner5 memory)	40
<input type="checkbox"/>	L94	L93 and descriptor\$1	40
<input type="checkbox"/>	L93	(image near5 header) and (data near5 header) and (data near5 structure) and (target near5 address) and @py<=2004	71
		<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L92	US-6704459-B1.did.	1
<input type="checkbox"/>	L91	US-6704459-B1.did.	1
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L90	(binary and data and structure and memory).ti. and @py<=2004	6
		<i>DB=USPT; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L89	US-6785791-B2.did.	1
<input type="checkbox"/>	L88	US-6785791-B2.did.	1
<input type="checkbox"/>	L87	US-6785791-B2.did.	1
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>	
<input type="checkbox"/>	L86	L85 and (data near5 segment\$1)	8

□	L85	L84 and (binary near5 data)	16
□	L84	L83 and (data near5 structure)	114
□	L83	L82 and descriptor	136
□	L82	(target near5 data) and (target near5 memory) and segment\$1 and header and @py<=2004	506
□	L81	L79 and descriptor\$1	4
□	L80	L79 descriptor\$1	35901
□	L79	L78 and (target near5 segment\$1) and (source near5 segment\$1)	7
□	L78	l70 and (target near5 field\$1) and (source near5 field\$1)	43
□	L77	L74 and (data near5 structure)	1
□	L76	L74 and header	0
□	L75	L74 and header and (data near5 structure)	0
□	L74	(source and target and memory).ti. and @py<=2004	49
□	L73	L71 and (memory near5 header)	8
□	L72	L71 and (target near5 header) and (source near5 header)	0
□	L71	L70 and (data near5 structure) and (binary near5 data)	161
□	L70	(target near5 memory) and (source near5 memory) and (data near5 segment\$1) and @py<=2004	2175
□	L69	7155709.pn.	2
□	L68	L67 and (binary near5 data)	8
□	L67	L66 and (data near5 segment\$1)	8
□	L66	L65 and (data near5 structure)	28
□	L65	L63 and L53	28
□	L64	L63 and (segment near5 header)	0
□	L63	L62 and (target near5 data)	118
□	L62	bootstrap and directory and data and structure and @py<=2003	480
□	L61	L60 and address	4
□	L60	L59 and memory	5
□	L59	L58 and descriptor\$1	7
□	L58	L57 and (data near5 field\$1)	38
□	L57	L56 and source and header	55
□	L56	(binary near5 structure) and segment\$1 and header and target and @py<2003	67
□	L55	L54 and (version near5 indicator\$1)	9
□	L54	L53 and target and source and descriptor\$1 and byte\$1	102
□	L53	(data near5 header) and segment\$1 and field\$1 and structure and ascii and binary and stor\$3 and @py<=2003	574
□	L52	microprocessor\$1 and bootstrap and ascii and checksum and (data near5 structure) and (data near5 header) and @py<=2003	0

microprocessor\$1 and bootstrap and ascii and checksum and (data near5

<input type="checkbox"/>	L51	structure) and (data near5 header) and target and source and stor\$3 and @py<=2003	0
<input type="checkbox"/>	L50	(assembly near5 code) and (image near5 code) and (data near5 structure) and programm\$3 and error\$1 and updat\$3 and ascii and bootstrap and index and checksum and filed\$1 and data and flag and header and @py<=2003	8
<input type="checkbox"/>	L49	L48 and (data near5 structure)	7
<input type="checkbox"/>	L48	image\$1 near5 s\$record\$1	76
<input type="checkbox"/>	L47	L46 and s\$record\$1	0
<input type="checkbox"/>	L46	L45 and ascii	23
<input type="checkbox"/>	L45	L44 and link\$3	27
<input type="checkbox"/>	L44	L43 and byte\$1	27
<input type="checkbox"/>	L43	L40 and memory and stor\$3 and flag	27
<input type="checkbox"/>	L42	L40 and (header near5 flag)	0
<input type="checkbox"/>	L41	L40 and non\$binary	0
<input type="checkbox"/>	L40	L38 and bootstrap	27
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<input type="checkbox"/>	L38	L37 and (data near5 field\$1)	30
<input type="checkbox"/>	L37	L36 and index\$3	30
<input type="checkbox"/>	L36	L35 and (target near5 data)	37
<input type="checkbox"/>	L35	L34 and (segment near5 size)	67
<input type="checkbox"/>	L34	L32 and (error near5 detection)	259
<input type="checkbox"/>	L33	L32 and (error near5 detectiion)	0
<input type="checkbox"/>	L32	L31 and (data near5 structure)	1036
<input type="checkbox"/>	L31	(image near5 data) and (data near5 segment\$1) and header and @py<=2003	1829
<input type="checkbox"/>	L30	(image near5 header) and (alignment near5 segment\$1) and (error near5 correction) and memory and field\$1 and s\$record\$1 and @py<=2002	0
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<input type="checkbox"/>	L28	L27 and (data near5 structure)	54
<input type="checkbox"/>	L27	(4724521 0r 5132716 or 4814754 or 5010553 or 4929946 or 4829526).uref.	138
<input type="checkbox"/>	L26	(4724521 0r 5132716 or 4814754 or 5010553 or 4929946 or 4829526).pn.	8
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<input type="checkbox"/>	L24	(20030051236 or 6499137 or 5862143).pn.	6
<input type="checkbox"/>	L23	L21 and L3	1
<input type="checkbox"/>	L22	L21 and L1	0
<input type="checkbox"/>	L21	L20 and (source same target)	18
<input type="checkbox"/>	L20	L19 and (data near5 header)	269
<input type="checkbox"/>	L19	L18 and (data near5 structure)	365
<input type="checkbox"/>	L18	L17 and (data near5 segment\$1)	642

<input type="checkbox"/>	L17	image near5 header	7420
<input type="checkbox"/>	L16	L15 and fields	6
<input type="checkbox"/>	L15	L14 and header	6
<input type="checkbox"/>	L14	L13 and target	22
<input type="checkbox"/>	L13	L3 and directory	29
<input type="checkbox"/>	L12	L11 and (target near5 data)	2
<input type="checkbox"/>	L11	L10 and (source near5 data)	25
<input type="checkbox"/>	L10	L3 and version\$1	71
<input type="checkbox"/>	L9	L6 and (s\$record)	1
<input type="checkbox"/>	L8	L6 and (segment near5 header)	2
<input type="checkbox"/>	L7	L6 and (mrb near5 image)	2
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<input type="checkbox"/>	L5	L3 and source	69
<input type="checkbox"/>	L4	L3 and L1	0
<input type="checkbox"/>	L3	binary data structure	120
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<input type="checkbox"/>	L1	(bootstrap and data and structure and (version\$1 or revesion\$1) and target and source and header and field\$1 and segment\$1 and descriptor\$1 and location and code and image and error and detection and correction) and @py<=2004	85

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» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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- ☐ 1. **The narcoleptic cognitive pupillary response**
O'Neill, W.D.; Trick, K.P.;
[Biomedical Engineering, IEEE Transactions on](#)
Volume 48, Issue 9, Sept. 2001 Page(s):963 - 968
Digital Object Identifier 10.1109/10.942585
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(100 KB) IEEE JNL
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- ☐ 2. **Design of a hyper media tool to support requirements elicitation meeting:**
Kaiya, H.; Saeki, M.; Ochimizu, K.;
[Computer-Aided Software Engineering, 1995. Proceedings., Seventh Internatic](#)
10-14 July 1995 Page(s):250 - 259
Digital Object Identifier 10.1109/CASE.1995.465308
[AbstractPlus](#) | Full Text: [PDF](#)(816 KB) IEEE CNF
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- ☐ 3. **A cost model for selecting checkpoint positions in time warp parallel sim**
Quaglia, F.;
[Parallel and Distributed Systems, IEEE Transactions on](#)
Volume 12, Issue 4, April 2001 Page(s):346 - 362
Digital Object Identifier 10.1109/71.920586
[AbstractPlus](#) | [References](#) | Full Text: [PDF](#)(424 KB) IEEE JNL
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- ☐ 4. **Detailed estimate of pulse-sequence data acquisition system designed fo**
channel measurement
Kitamura, Y.; Sakae, T.; Nohtomi, A.; Matoba, M.; Matsumoto, Y.;
[Nuclear Science Symposium, 1996. Conference Record., 1996 IEEE](#)
Volume 2, 2-9 Nov. 1996 Page(s):677 - 680 vol.2
Digital Object Identifier 10.1109/NSSMIC.1996.591400
[AbstractPlus](#) | Full Text: [PDF](#)(272 KB) IEEE CNF
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- ☐ 5. **Autoassociative neural memory capacity and dynamics**
Hassoun, M.H.; Youssef, A.M.;
[Neural Networks, 1990., 1990 IJCNN International Joint Conference on](#)
17-21 June 1990 Page(s):763 - 769 vol.1
Digital Object Identifier 10.1109/IJCNN.1990.137660

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- ☐ 6. **An evaluation of Starburst's memory resident storage component**
Lehman, T.J.; Shekita, E.J.; Cabrera, L.-F.;
[Knowledge and Data Engineering, IEEE Transactions on](#)
Volume 4, Issue 6, Dec. 1992 Page(s):555 - 566
Digital Object Identifier 10.1109/69.180606
[AbstractPlus](#) | Full Text: [PDF](#)(1088 KB) IEEE JNL
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- ☐ 7. **Adaptive structuring of binary search trees using conditional rotations**
Cheetham, R.P.; Oommen, B.J.; Ng, D.T.H.;
[Knowledge and Data Engineering, IEEE Transactions on](#)
Volume 5, Issue 4, Aug. 1993 Page(s):695 - 704
Digital Object Identifier 10.1109/69.234780
[AbstractPlus](#) | Full Text: [PDF](#)(968 KB) IEEE JNL
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- ☐ 8. **Brazil holds all-electronic national election**
Riebeek, H.;
[Spectrum, IEEE](#)
Volume 39, Issue 11, Nov. 2002 Page(s):25 - 26
Digital Object Identifier 10.1109/MSPEC.2002.1045582
[AbstractPlus](#) | Full Text: [PDF](#)(213 KB) IEEE JNL
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- ☐ 9. **An automatic meteorological data collection system that is installed at GI System monitoring stations**
Michelena, E.D.; Gutman, S.I.;
[Oceans '02 MTS/IEEE](#)
Volume 4, 29-31 Oct. 2002 Page(s):1930 - 1934 vol.4
Digital Object Identifier 10.1109/OCEANS.2002.1191927
[AbstractPlus](#) | Full Text: [PDF](#)(403 KB) IEEE CNF
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- ☐ 10. **Optical holographic grating formation in dye-and fullerene-doped liquid crystal system**
Simoni, F.; Francescangeli, O.; Reznikov, Y.; Slussarenko, S.; Khoo, I.C.; Woc B.D.;
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Volume 1, 18-19 Nov. 1996 Page(s):265 vol.1
Digital Object Identifier 10.1109/LEOS.1996.565232
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- ☐ 11. **A microprocessor-based instrument for nystagmus analysis**
Michaels, D.L.; Tole, J.R.;
[Proceedings of the IEEE](#)
Volume 65, Issue 5, May 1977 Page(s):730 - 735
[AbstractPlus](#) | Full Text: [PDF](#)(1148 KB) IEEE JNL
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- ☐ 12. **Review of current proposed technologies for mass storage systems**
Matick, R.E.;
[Proceedings of the IEEE](#)
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- ☐ **13. Analog-to-digital conversion with unlatched SQUID's**
Hurrell, J.P.; Pridmore-Brown, D.C.; Silver, A.H.;
Electron Devices, IEEE Transactions on
Volume 27, Issue 10, Oct 1980 Page(s):1887 - 1896
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- ☐ **14. Computer recognition of speech using zero-crossing information**
Ewing, G.; Taylor, J.;
Audio and Electroacoustics, IEEE Transactions on
Volume 17, Issue 1, Mar 1969 Page(s):37 - 40
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- ☐ **15. Performance of Error-Correcting Codes on Channels with Memory**
Drukarev, A.; Kai Yiu;
Communications, IEEE Transactions on [legacy, pre - 1988]
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- ☐ **16. Application of thin film head technology to a high performance head/trac**
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Volume 14, Issue 4, Jul 1978 Page(s):188 - 190
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- ☐ **17. Ferrimagnetic garnet films for magnetooptic information storage**
Krumme, J.-P.; Schmitt, H.;
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Volume 11, Issue 5, Sep 1975 Page(s):1097 - 1102
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- ☐ **18. Trends and projections in magnetic recording storage on particulate mer**
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Volume 16, Issue 1, Jan 1980 Page(s):26 - 29
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- ☐ **19. A bubble memory module**
Hayes, P.; Segura, R.; Stermer, R.; Bohning, O.;
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Volume 16, Issue 5, Sep 1980 Page(s):773 - 775
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- ☐ **20. Ferrite core memories that shaped an industry**
Pugh, E.;
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Volume 20, Issue 5, Sep 1984 Page(s):1499 - 1502
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- ☐ **21. Application of holographic optical techniques to bulk memory**
Anderson, L.;
[Magnetics, IEEE Transactions on](#)
Volume 7, Issue 3, Sep 1971 Page(s):601 - 605
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- ☐ **22. Magnetostrictive phenomena in metallic materials and some of their devi**
Hornreich, R.; Rubinstein, H.; Spain, R.;
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- ☐ **23. Soniscan: A ferroacoustic thin-film memory**
Rubinstein, H.; Hornreich, R.; Teixeira, J.; Cohler, E.;
[Magnetics, IEEE Transactions on](#)
Volume 6, Issue 3, Sep 1970 Page(s):475 - 488
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- ☐ **24. Optical storage of digital information**
Spitz, E.; D'Auria, L.; Huignard, J.;
[Magnetics, IEEE Transactions on](#)
Volume 8, Issue 3, Sep 1972 Page(s):425 - 425
[AbstractPlus](#) | Full Text: [PDF\(168 KB\)](#) IEEE JNL
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- ☐ **25. Editor's Overview, Dec. 1981**
Eisenhart, R.L.;
[Microwave Theory and Techniques, IEEE Transactions on](#)
Volume 29, Issue 12, Dec 1981 Page(s):1251 - 1251
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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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1. The binary compatibility standard

Anderson, A.; Cruess, M.; Wiencek, E.;

COMPCON Spring '89. Thirty-Fourth IEEE Computer Society International Conference on Intellectual Leverage. Digest of Papers.

27 Feb.-3 March 1989 Page(s):32 - 37

Digital Object Identifier 10.1109/CMPCON.1989.301899

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Display Format: ☒ Citation ☐ Citation & Abstract

» Key

IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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View: 1-

- ☐ 1. **MP/C: A Multiprocessor/Computer Architecture**
Arden, B.W.; Ginosar, R.;
[Computers, IEEE Transactions on](#)
Volume C-31, Issue 5, May 1982 Page(s):455 - 473
[AbstractPlus](#) | Full Text: [PDF\(4944 KB\)](#) IEEE JNL
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- ☐ 2. **Group contexts: an architectural approach to virtual sharing**
Mohamed, A.; Sagahyroon, A.;
[Communications, Computers and Signal Processing, 1997. '10 Years PACRIM Networking the Pacific Rim', 1997 IEEE Pacific Rim Conference on](#)
Volume 1, 20-22 Aug. 1997 Page(s):289 - 293 vol.1
Digital Object Identifier 10.1109/PACRIM.1997.619957
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- ☐ 3. **Hierarchical memory mapping during synthesis in FPGA-based reconfig**
Ouaiss, I.; Vemuri, R.;
[Design, Automation and Test in Europe, 2001. Conference and Exhibition 2001](#)
13-16 March 2001 Page(s):650 - 657
Digital Object Identifier 10.1109/DATE.2001.915092
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- ☐ 4. **Tuning memory performance of sequential and parallel programs**
Martonosi, M.; Gupta, A.; Anderson, T.E.;
[Computer](#)
Volume 28, Issue 4, April 1995 Page(s):32 - 40
Digital Object Identifier 10.1109/2.375175
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(816 KB\)](#) IEEE JNL
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- ☐ 5. **Dynamic partitioning of non-uniform structured workloads with spacefilli**
Pilkington, J.R.; Baden, S.B.;
[Parallel and Distributed Systems, IEEE Transactions on](#)
Volume 7, Issue 3, March 1996 Page(s):288 - 300
Digital Object Identifier 10.1109/71.491582

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(1300 KB\)](#) IEEE JNL
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- ☐ **6. Bistability and hysteresis in the characteristics of segmented-anode later**
Gartner, M.; Vietzke, D.; Reznik, D.; Stoisiek, M.; Oppermann, K.-G.; Gerlach,
[Electron Devices, IEEE Transactions on](#)
Volume 45, Issue 7, July 1998 Page(s):1575 - 1579
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- ☐ **7. High performance distributed objects using distributed shared memory a
method invocation**
Fleisch, B.D.; Hyde, R.L.;
[System Sciences, 1998., Proceedings of the Thirty-First Hawaii International C](#)
Volume 7, 6-9 Jan. 1998 Page(s):574 - 578 vol.7
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- ☐ **8. Bistability and hysteresis in the characteristics of segmented-anode later**
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26-29 May 1997 Page(s):255 - 259
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- ☐ **9. PRISM-a design for scalable shared memory**
Kattamuri, E.; Beng-Hong Lim; Pattnaik, P.; Snir, M.;
[Innovative Architecture for Future Generation High-Performance Processors at](#)
22-24 Oct. 1997 Page(s):29
Digital Object Identifier 10.1109/IWIA.1997.670404

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- ☐ **10. System partitioning to maximize sleep time**
Farrahi, A.H.; Sarrafzadeh, M.;
[Computer-Aided Design, 1995. ICCAD-95. Digest of Technical Papers., 1995 I](#)
[International Conference on](#)
5-9 Nov. 1995 Page(s):452 - 455
Digital Object Identifier 10.1109/ICCAD.1995.480155

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- ☐ **11. The case for segments**
Cooper, T.; Wise, M.;
[Object-Orientation in Operating Systems, 1995., Fourth International Worksho](#)
14-15 Aug. 1995 Page(s):94 - 102
Digital Object Identifier 10.1109/IWOOS.1995.470573

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- ☐ **12. The architecture of the shared memory subsystem of the Minitaur II paral
system**
Kulick, J.H.; Pennycook, B.;
[Southeastcon '93, Proceedings., IEEE](#)
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- ☐ **13. Structure of computer programs: A survey**
Holt, R.C.;
[Proceedings of the IEEE](#)
Volume 63, Issue 6, June 1975 Page(s):879 - 893
[AbstractPlus](#) | Full Text: [PDF](#)(1833 KB) [IEEE JNL](#)
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- ☐ **14. Structured logic design of integrated circuits using the storage/logic array**
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IEEE JNL IEEE Journal or Magazine

IEE JNL IEE Journal or Magazine

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